



6/28/15  
4/Election  
P. Walker  
11-25-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

Shinichi Imai et al. )

) Group Art Unit: 2815

Serial No. 10/032,562 )

) Examiner: Ortiz, Edgardo

Filed: January 2, 2002 )

For: SEMICONDUCTOR DEVICE AND )

METHOD FOR MANUFACTURING )

THE SAME )

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with  
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Washington, D.C. 20231, on July 3, 2002

April Campbell

RESPONSE TO ELECTION REQUIREMENT

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the Office Action of June 18, 2002, please amend the subject application  
as follows:

In response to the election requirement in the Office Action of June 18, 2002, Applicant  
hereby elects without traverse the Group I claims - that is, claims 1-8, drawn to a method of  
manufacturing a semiconductor device, classified in class 438, subclass +1.

Examination on the merits is requested.

Respectfully submitted,

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